

Data Sheet

Description

KGF65A4L, MGF65A4L, and FGF65A4L are 650 V Field Stop IGBTs. Sanken original trench structure decreases gate capacitance, and achieves low saturation voltage and switching losses reduction. Thus, Field Stop IGBTs can improve the efficiency of your circuit.

Features

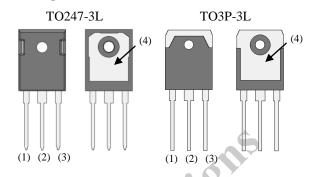
- Low Saturation Voltage
- High Speed Switching
- With Integrated Fast Recovery Diode
- Bare lead frame: Pb-free (RoHS compliant)

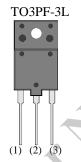
• V _{CE} 65	50 V
• I _C (T _C = 100 °C)	10 A
Short Circuit Withstand Time	5 μs
• V _{CE(sat)} 1.6 V	typ.
• $t_f (T_J = 175 ^{\circ}\text{C})$ 160 ns	
• V _F 1.7 V	

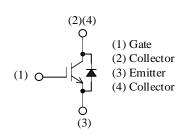
Applications

- Uninterruptible Power Supply (UPS)
- Inverter Circuit
- Bridge Circuit

Package







Selection Guide

(1) (2) (3)	
	(1) Gate (2) Collector (3) Emitter (4) Collector
Selection Guide	
Part Number	Package
KGF65A4L	TO247-3L
MGF65A4L	TO3P-3L
FGF65A4L	TO3PF-3L
	Selection Guide Part Number KGF65A4L MGF65A4L

KGF65A4L, MGF65A4L, FGF65A4L

Absolute Maximum Ratings

Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Conditions	Rating	Unit	Remarks
Collector to Emitter Voltage	V_{CE}		650	V	
Gate to Emitter Voltage	V_{GE}		±30	V	
Continuous Collector Current (1)	Ţ	$T_C = 25 ^{\circ}C$	65	A	
Continuous Conector Current	I_{C}	$T_C = 100 ^{\circ}C$	40	A	
Pulsed Collector Current	$I_{C(PULSE)}$	$PW \le 1 \text{ ms},$ duty cycle $\le 1\%$	120	A	
Diode Continuous Forward Current (1)	T	$T_C = 25$ °C	40 ⁽²⁾	A	
Diode Continuous Forward Current	I_{F}	$T_C = 100 ^{\circ}C$	40	A	5
Diode Pulsed Forward Current	I _{F(PULSE)}	PW ≤ 1 ms, duty cycle ≤ 1%	120	Α	
Short Circuit Withstand Time	$t_{ m SC}$	$V_{GE} = 15 \text{ V},$ $V_{CE} = 400 \text{ V}$ $T_{J} = 175 ^{\circ}\text{C}$	5	μs	
Power Dissipation	P_{D}	T _C = 25 °C	288	W	MGF65A4L KGF65A4L FGF65A4L
Operating Junction Temperature	T_{J}		175	°C	
Storage Temperature Range	T_{STG}		−55 to 150	°C	
Isolation Voltage	V _{ISO(RMS)}	Between surface of case and all pins that are shorted; AC, 60 Hz, 1 min	1500	V	FGF65A4L

Thermal Characteristics

Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	Remarks
Thermal Resistance of IGBT	R _{0JC} (IGBT)		_	_	0.52	°C/W	MGF65A4L KGF65A4L
(Junction to Case)	Y		_		2.08		FGF65A4L
Thermal Resistance of Diode					1.15		MGF65A4L
(Junction to Case)	$R_{\theta JC}(Di)$				1.13	°C/W	KGF65A4L
(Junction to Case)			_		2.28		FGF65A4L

 $^{^{(1)}}$ I_C and I_F are determined by the maximum junction temperature for TO3P-3L package. $^{(2)}$ Determined by bonding wires capability.

KGF65A4L, MGF65A4L, FGF65A4L

Electrical Characteristics

Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Collector to Emitter Breakdown Voltage	V _{(BR)CES}	$I_C = 100 \ \mu A, \ V_{GE} = 0 \ V$	650		_	V	
Collector to Emitter Leakage Current	I_{CES}	$V_{CE} = 650 \text{ V}, V_{GE} = 0 \text{ V}$	_	_	100	μΑ	
Gate to Emitter Leakage Current	I_{GES}	$V_{GE} = \pm 30 \text{ V}$	_		±500	nA	
Gate Threshold Voltage	V _{GE(TH)}	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$	4.0	5.5	7.0	V	
Collector to Emitter Saturation Voltage	V _{CE(sat)}	$V_{GE} = 15 \text{ V}, I_{C} = 40 \text{ A}$	_	1.6	1.96	V	
Input Capacitance	C_{ies}	$V_{CE} = 20 \text{ V},$	_	2300	43		
Output Capacitance	C_{oes}	$V_{GE} = 0 V$,	_	250	0 -	pF	
Reverse Transfer Capacitance	C _{res}	f = 1.0 MHz,		110			
Gate charge	Q_{g}	$V_{CE} = 520 \text{ V}, I_{C} = 40 \text{ A}, $ $V_{GE} = 15 \text{ V}$	-<	75		nC	
Turn-on Delay Time	$t_{d(on)}$		*	40	_		
Rise Time	t _r	4		40	_	ns	
Turn-off Delay Time	$t_{d(off)}$	$T_{\rm J} = 25 ^{\circ}{\rm C}$		100	_		
Fall Time	t_{f}	see Figure 1.		50	_		
Turn-on Energy (3)	Eon	60	_	0.9	_	T	
Turn-off Energy	$E_{\rm off}$		_	0.9	_	mJ	
Turn-on Delay Time	$t_{d(on)}$		_	40			
Rise Time	$t_{\rm r}$	30	_	40			
Turn-off Delay Time	$t_{ m d(off)}$	$T_{\rm J}$ = 175 °C,	_	130		ns	
Fall Time	t_{f}	see Figure 1.	_	160	_		
Turn-on Energy (3)	Eon			1.6		τ.	
Turn-off Energy	$E_{\rm off}$			1.6		mJ	
Emitter to Collector Diode Forward Voltage	V_{F}	I _F = 40 A		1.7		V	
Emitter to Collector Diode Reverse Recovery Time	t _{rr}	$I_F = 40 \text{ A},$ $di/dt = 600 \text{ A/}\mu\text{s}$		60		ns	

 $^{\left(3\right) }$ Energy losses include the reverse recovery of diode.

Test Circuits and Waveforms

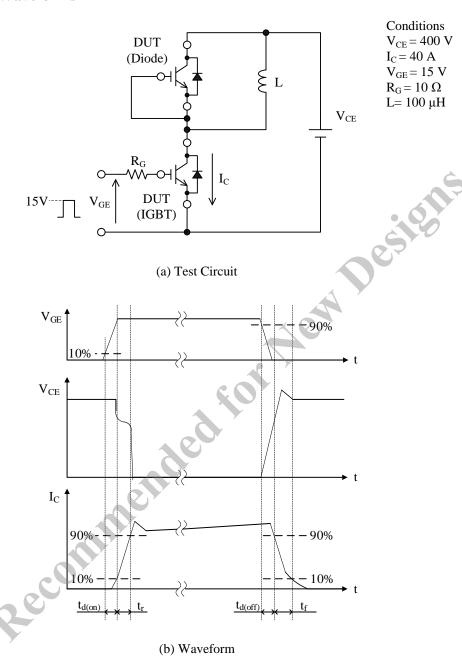


Figure 1. Test Circuits and Waveforms of dv/dt and Switching Time

Rating and Characteristic Curves

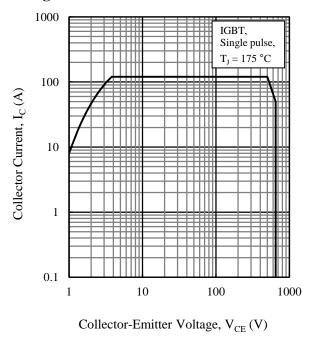


Figure 2. IGBT Reverse Bias Safe Operating Area

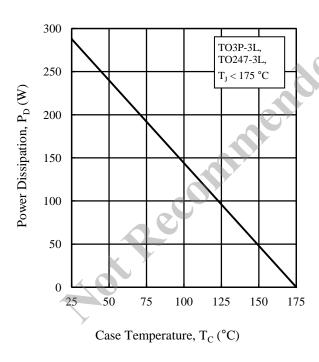


Figure 4. Power Dissipation vs. Case Temperature

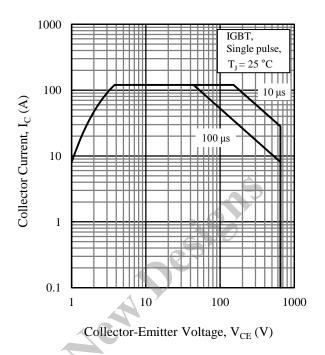


Figure 3. IGBT Safe Operating Area

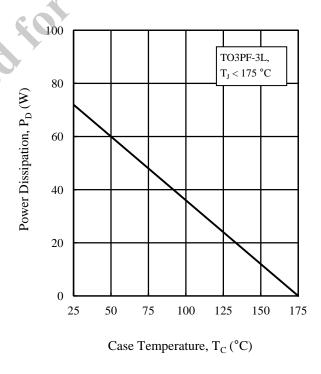


Figure 5. Power Dissipation vs. Case Temperature

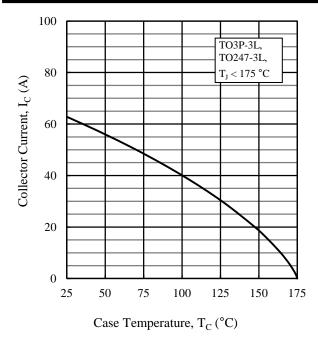


Figure 6. Collector Current vs. Case Temperature

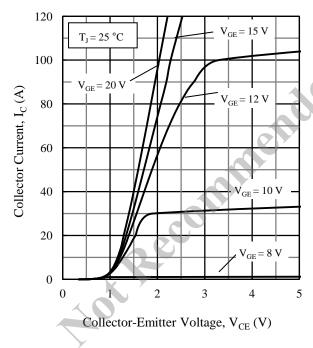


Figure 8. Output Characteristics ($T_J = 25$ °C)

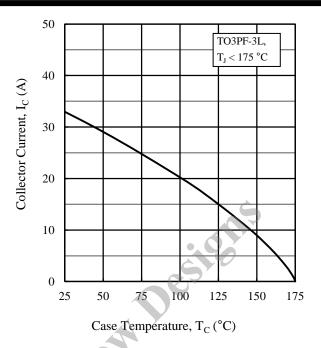


Figure 7. Collector Current vs. Case Temperature

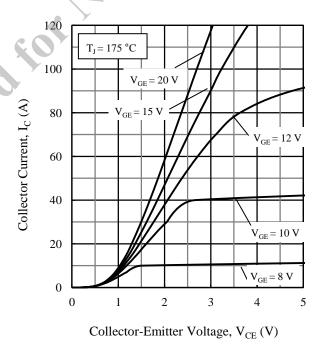


Figure 9. Output Characteristics ($T_I = 175$ °C)

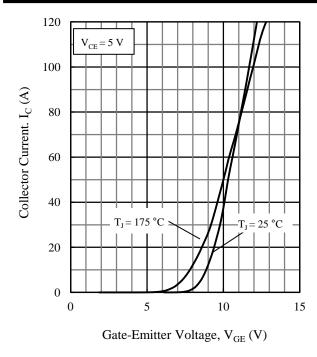


Figure 10. Transfer Characteristics

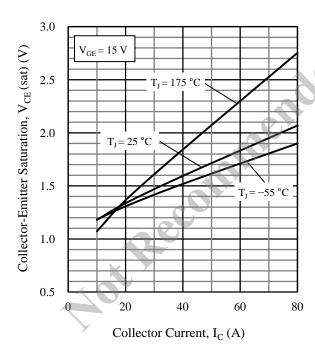
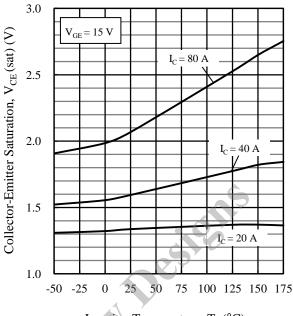


Figure 12. Saturation Voltage vs. Collector Current



Junction Temperature, T_J (°C)

Figure 11. Saturation Voltage vs. Junction Temperature

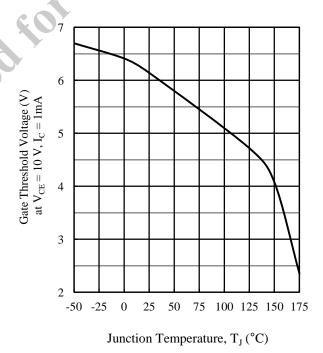


Figure 13. Gate Threshold Voltage vs. Junction Temperature

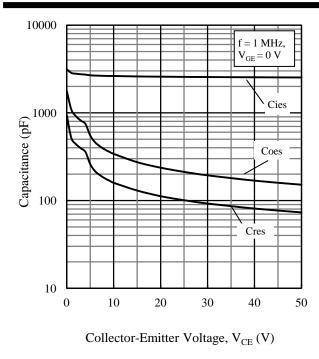


Figure 14. Capacitance Characteristics

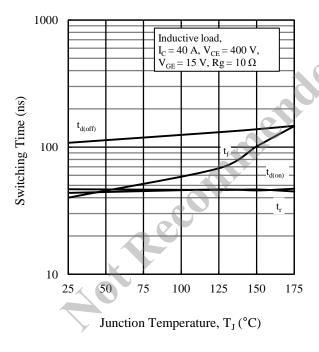


Figure 16. Switching Time vs. Junction Temperature

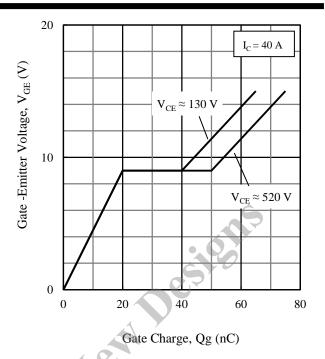


Figure 15. Typical Gate Charge

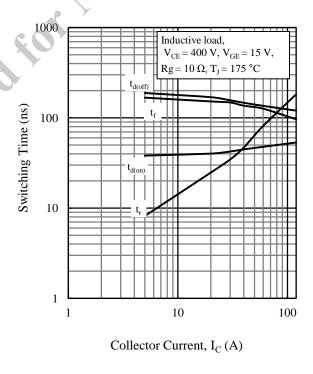


Figure 17. Switching Time vs. Collector Current

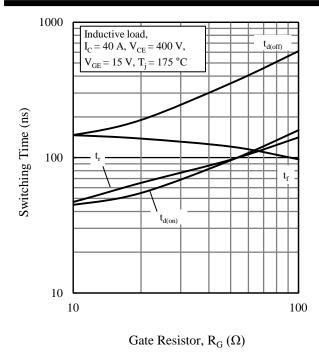


Figure 18. Switching Time vs. Gate Resistor

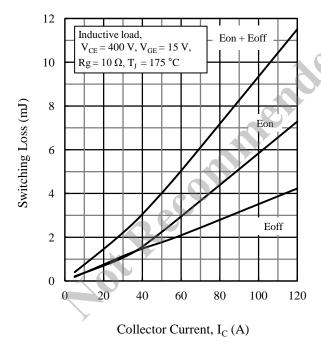


Figure 20. Switching Loss vs. Collector Current

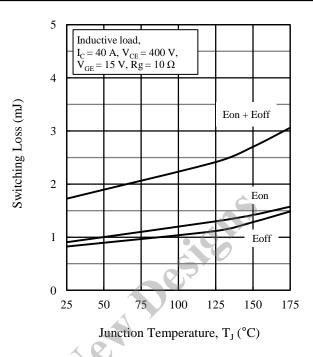


Figure 19. Switching Loss vs. Junction Temperature

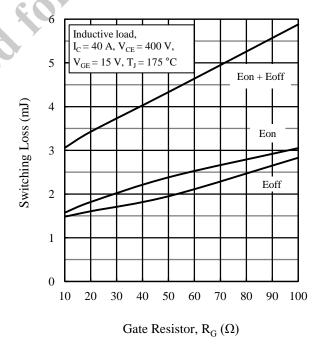


Figure 21. Switching Loss vs. Gate Resistor

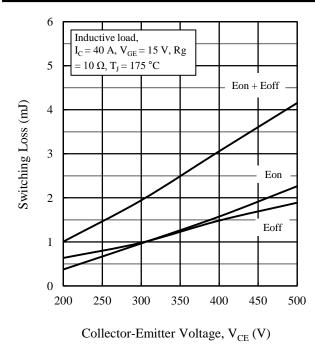
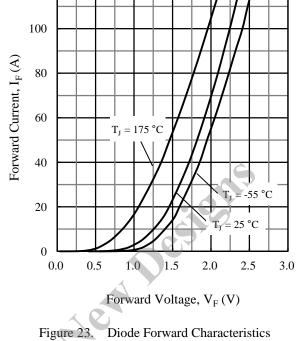


Figure 22. Switching Loss vs. Collector-Emitter Voltage



120

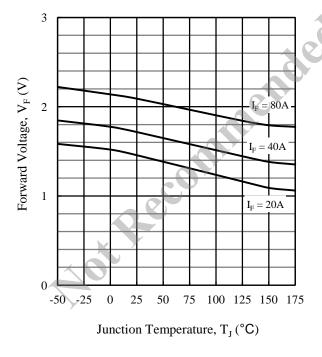


Figure 24. Diode Forward Voltage vs. Junction Temperature

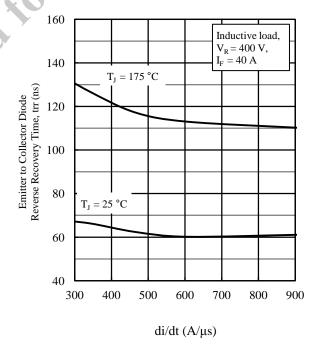
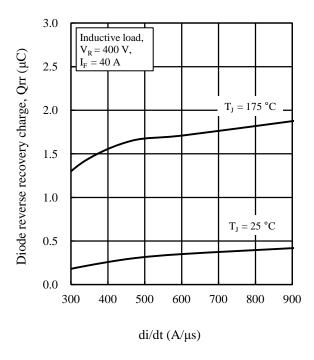
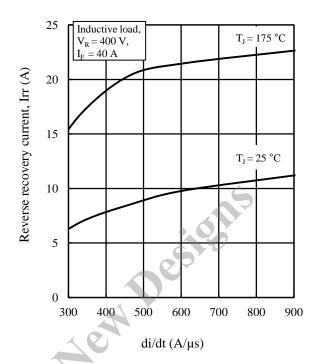


Figure 25. Emitter to Collector Diode Reverse Recovery Time vs. di/dt





 $\begin{array}{ccc} Figure~26. & Diode~Reverse~Recovery~Charge~vs.\\ & & di/dt \end{array}$

Figure 27. Recovery Current vs. di/dt

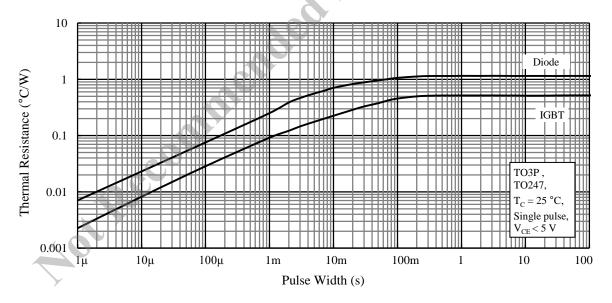


Figure 28. Transient Thermal Resistance

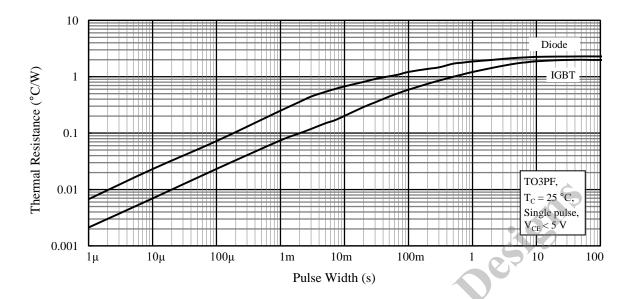
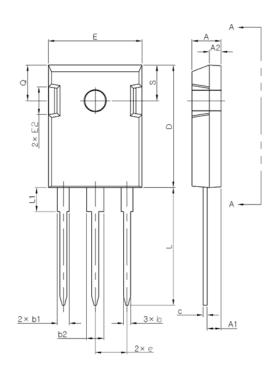
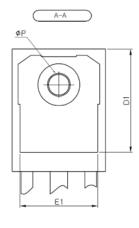


Figure 29. Transient Thermal Resistance

Physical Dimensions

• TO247-3L



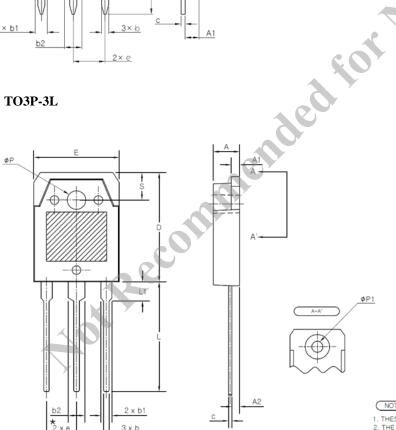


SYMBOL	MIN	NOM	MAX
Α	4.83	5.02	5.21
A1	2.29	2.41	2.54
A2	1.91	2.04	2.16
b	1.14	1.27	1.40
b1	1.91	2.10	2.20
b2	2.92	3.10	3.20
С	0.61	0.71	0.80
D	20.80	21.07	21.34
D1	17.43	17.63	17.83
Е	15.75	15,94	16.13
E1	13.06	13.26	13.46
E2	4.32	4.58	4.83
е	5.25	5.45	5.65
L	19.81	20.19	20.57
L1	3,81	4.07	4.32
ØP_<	3.55	3.60	3.65
0	5.59	5.90	6.20
S		6.15 BSC	

NOTE

1. THESE DIMENSION DO NOT INCLUDE MOLD PROTRUSION

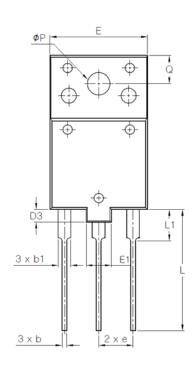
• TO3P-3L

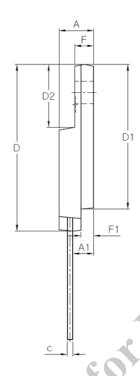


SYMBOL	MIN	NOM	MAX
Α	4.60	4.80	5.00
A1	1.45	1.50	1.65
A2	1.20	1.40	1.60
b	0.80	1.00	1.20
b1	1.80	2.00	2.20
b2	2.80	3.00	3.20
С	0.55	0.60	0.75
D	19.70	19.90	20.10
Е	15.40	15.60	15.80
* _e	5.25	5.45	5.65
L	19.80	20.00	20.20
L1	3.30	3.50	3.70
ΦP	3.30	3.40	3.50
ØP1	3.10	3.20	3.30
S	4.80	5.00	5.20

1. THESE DIMENSIONS DO NOT INCLUDE PROTRUSIONS OF THE MOLD. 2. THE $^{\circ}($)" MARK IS THE REFERENCE

• TO3PF-3L



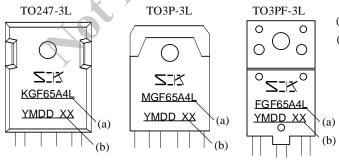


SYMBOL	MIN	MOM	MAX
Α	5.30	5.50	5.70
A1	3.10	3,30	3,50
b	0.65	0.75	0.95
b1	1,80	2,00	2,20
С	0.80	0.90	1,10
D	26.30	26.50	26.70
D1	22.80	23.00	23.20
D2	9.80	10.00	10.20
D3	1.80	2.00	2.20
Е	15,30	15.50	15.70
E1	3,80	4.00	4.20
е	5,25	5,45	5,65
F.4	2.80	3,00	3,20
F1	1.80	2.00	2.20
	19.10	19.30	19.50
L1	4.80	5.00	5.20
Q	4.30	4.50	4.70
ΦP	3,40	3,60	3,80

NOTES:

- Dimensions in millimeters
- Bare lead frame for TO247, TO3P and TO3PF: Pb-free (RoHS compliant)
- When soldering the products, make sure to minimize the working time within the following limits: Flow: 260 ± 5 °C / 10 ± 1 s, 2 times
 - Soldering Iron: 380 ± 10 °C / 3.5 ± 0.5 s, 1 time (Soldering should be at a distance of at least 1.5 mm from the body of the products.)
- Soldering should be at a distance of at least 1.5 mm from the body of the products.
- The recommended screw torque for TO247, TO3P and TO3PF: 0.686 to 0.882 N·m (7 to 9 kgf·cm)

Marking Diagram



- (a) Part Number
- (b) Lot Number

Y is the last digit of the year of manufacture (0 to 9). M is the month of the year (1 to 9, O, N or D).

DD is the day of the month (01 to 31).

XX is the control number.

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- No anti-radioactive ray design has been adopted for the Sanken Products.
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