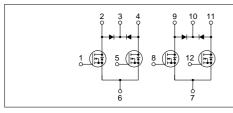
# 絶対最大定格

(	-14	a =	= 2	25	5

記号	定 格	単 位
VDSS	100	V
Vgss	± 10	V
ΙD	± 4	Α
ID( pulse )	±8(PW 1ms)	Α
Eas	16	mJ
İF	4(PW 0.5ms, Du 25%)	Α
IFSM	8 (PW 10ms, 単発パルス)	Α
VR	120	V
Рт	4(Ta=25 ,全回路動作, No Fin)	W
FI	28(Tc=25 ,全回路動作, Fin)	W
j-a	31.2(接合 - 外気間, Ta = 25 , 全回路動作)	/W
j-c	4.46(接合 - ケース間, Tc = 25 , 全回路動作)	/W
Tch	150	
Tstg	- 40 ~ + 150	

:Vdd = 20V, L = 1mH, ld = 5A, unclamped, P15図E参照

### 等価回路図



# 電気的特性

(Ta=25)

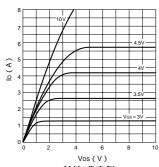
記号	規格値			単位	条件	
	min	typ	max	<del>+</del> 12	· · · · · · · · · · · · · · · · · · ·	
V( BR )DSS	100			V	$I_D = 250 \muA$ , $V_{GS} = 0V$	
Igss			± 500	nA	Vgs = ± 10V	
IDSS			250	μA	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	
VTH	1.0		2.0	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 250 μA	
Re(yfs)	1.1	1.7		S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4A	
RDS(ON)		0.47	0.55		Vgs = 10V, ID = 2A	
KD9(ON)		0.60	0.78		Vgs = 4V, ID = 2A	
Ciss		230		pF	V <sub>DS</sub> = 25V, f = 1.0MHz,	
Coss		60		pF	V <sub>GS</sub> = 0V	
ton		60		ns	ID = 4A, VDD 50V,	
toff		50		ns	Vgs = 10V, P16図3参照	
Vsp		1.2	2.0	V	I <sub>SD</sub> = 4A, V <sub>GS</sub> = 0V	
trr		250		ns	IsD = ± 100mA	
·					<u> </u>	

### フライバック電圧吸収用ダイオード

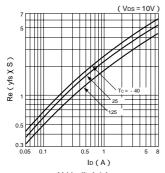
記号	規格値			単位	タ ル
記号	min	typ	max	平 12	条件
VR	120			V	IR = 10 μA
VF		1.0	1.2	V	I <sub>F</sub> = 1A
lR			10	μΑ	V <sub>R</sub> = 120V
trr		100		ns	If = ± 100mA

#### ■特性曲線■

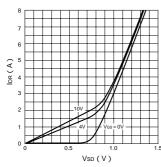
ID-VDS**特性(代表例)** 



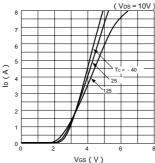
Re(yfs)-ID特性(代表例)



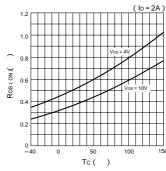
IDR-VSD特性(代表例)

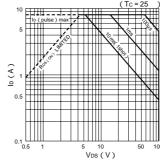


ID-Vgs**特性(代表例)** 

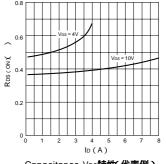


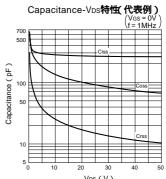
RDS(ON)-Tc特性(代表例)



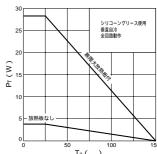


RDS(ON)-ID特性(代表例)

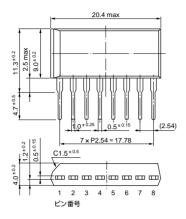




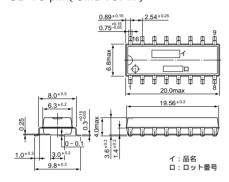




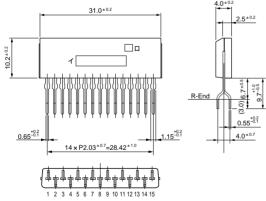
#### ·STA 8 pin (SIP8Pin)



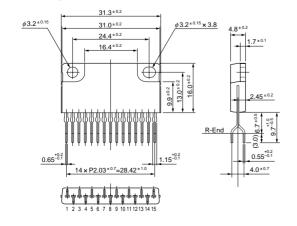
#### ·SD 16 pin (SMD16Pin)



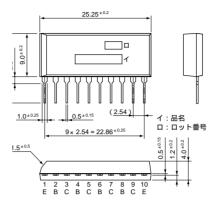
### ·SMA 15 pin (SIP15Pin)



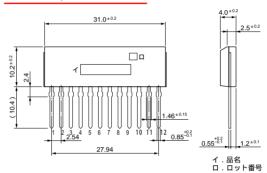
## ・SLA 15 pin (SIP15Pin フィン付)



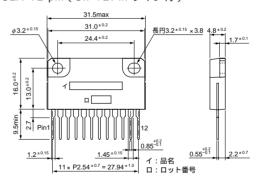
#### ·STA 10 pin (SIP10Pin)



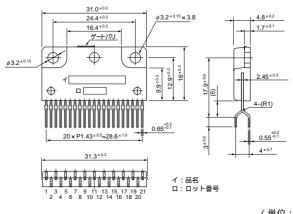
# ·SMA 12 pin (SIP12Pin)



#### ・SLA 12 pin (SIP12Pin フィン付)



## ・SLA 21 pin (SIP21Pin フィン付)



(単位:mm)