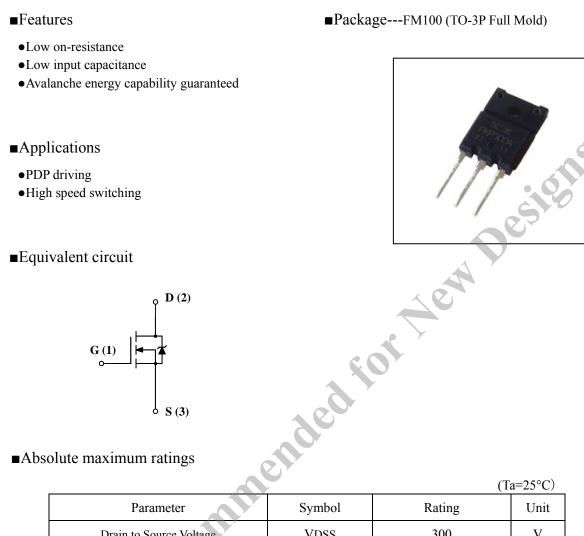


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	×	(1	a = 25 C
Parameter	Symbol	Rating	Unit
Drain to Source Voltage	VDSS	300	V
Gate to Source Voltage	VGSS	±30	V
Continuous Drain Current	ID	±30A	А
Pulsed Drain Current	ID(pulse) *1	±120A	А
Maximum Power Dissipation	PD	85 (Tc=25°C)	W
Single Pulse Avalanche Energy	EAS * ²	400	mJ
Avalanche Current	IAS	30	А
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to 150	°C

*1 PW \leq 100µs, duty cycle \leq 1%

*2 VDD=20V, L=830µH, ILp=30A, unclamped, RG=50Ω, See Fig.1

$\frac{\text{N-Channel MOS FET}}{FKP300A}$



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Electrical	characteristics

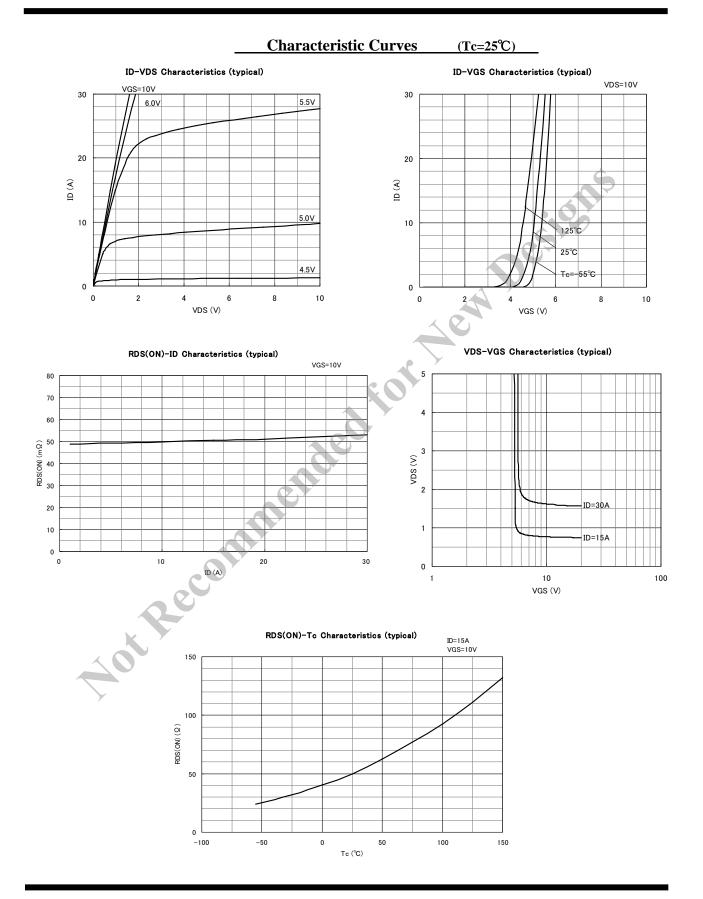
		ectrical characteristics	,		(Ta=25°	C)
Parameter	0 1 1		Limits			T T '4
	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Drain to Source breakdown Voltage	V(BR)DSS	ID=100µA,VGS=0V	300			V
Gate to Source Leakage Current	IGSS	VGS=±30V			±100	nA
Drain to Source Leakage Current	IDSS	VDS=300V, VGS=0V			100	μΑ
Gate Threshold Voltage	VTH	VDS=10V, ID=1mA	3.0		4.5	V
Forward Transconductance	Re(Yfs)	VDS=10V, ID=15A	20	33		S
Static Drain to Source On-Resistance	RDS(on)	ID=15A, VGS=10V		57	65	mΩ
Input Capacitance	Ciss	VDS=25V		3800		
Output Capacitance	Coss	VGS=0V		540		pF
Reverse Transfer Capacitance	Crss	f=1MHz		180		
Turn-On Delay Time	td(on)	ID=15A, VDD≈150V		40		
Rise Time	tr	RL= 10Ω , VGS= $10V$		60		ns
Turn-Off Delay Time	td(off)	RG=5Ω		160		
Fall Time	tſ	See Fig.2		60		
Source-Drain Diode Forward Voltage	VSD	ISD=30A,VGS=0V		1.0	1.5	V
Gate Threshold Voltage Temp. Coefficient	ΔVTH /ΔTch	VDS=10V, ID=1mA		-11		mV/°C

N-Channel MOS FET

FKP300A

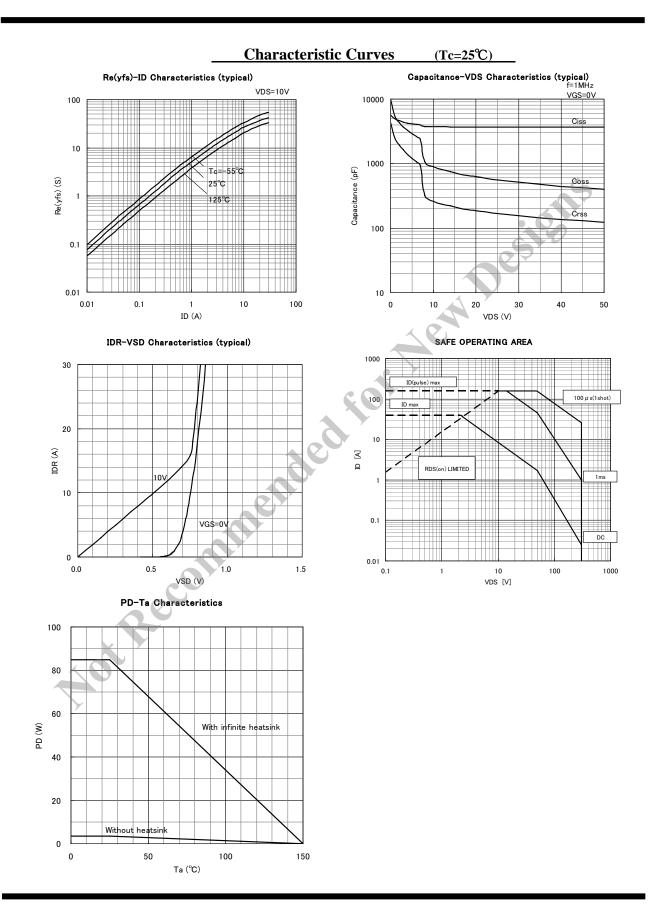
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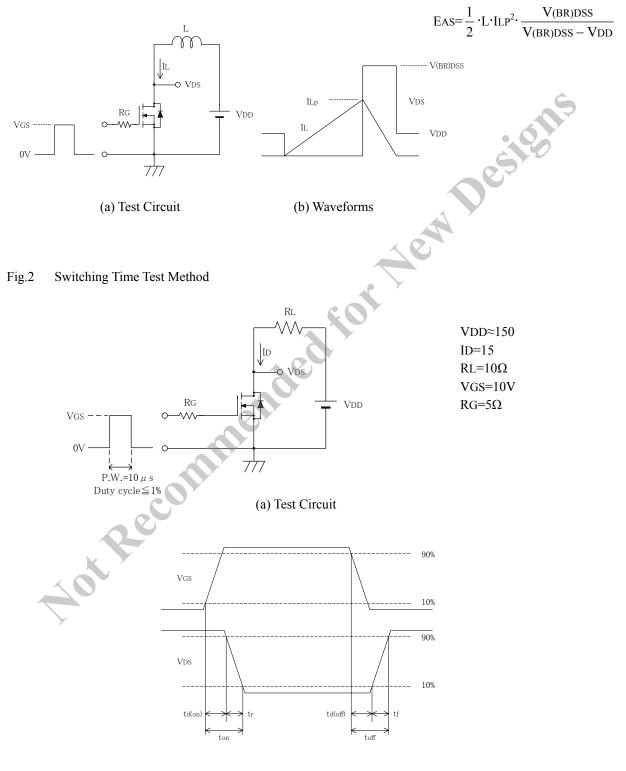
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SanKen

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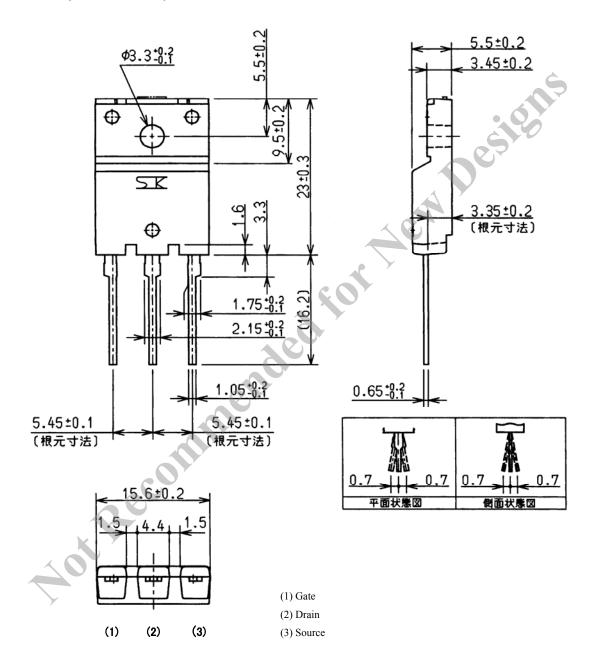
(b) Waveforms



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External dimensions

FM100 (TO-3P Full Mold)



Weight Approx. 6.5g



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Allegro MicroSystems, Inc. (Southern California)

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