

# **Data Sheet**

# **Description**

The KGF65B5H is 650 V field Stop IGBT with a fast recovery diode. Sanken original trench structure decreases gate capacitance, and achieves high speed switching and switching loss reduction. Thus, the field stop IGBT can improve the efficiency of your circuit.

#### **Features**

• V <sub>CE</sub>	650 V
• I <sub>C</sub> (T <sub>C</sub> = 100 °C)	50 A
• V <sub>CE(SAT)</sub>	
• V <sub>F</sub>	

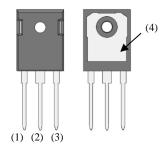
- Pb-free (RoHS Compliant)
- Low Saturation Voltage
- High Speed Switching
- With Fast Recovery Diode

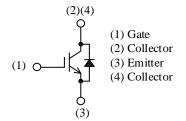
# **Applications**

- Solar Power Conditioners, UPS Inverter Circuit
- General-purpose Converter Circuit
- Welding Machine

### **Package**

TO247-3L





Not to scale

# KGF65B5H

# **Absolute Maximum Ratings**

Unless specifically noted,  $T_A = 25$  °C.

Parameter Parameter	Symbol	Conditions	Rating	Unit
Collector-to-Emitter Voltage	$V_{CE}$		650	V
Gate-to-Emitter Voltage	$V_{GE}$		±20	V
Continuous Collector Current	$I_{C}$	$T_C = 25$ °C	80(1)	A
		T <sub>C</sub> = 100 °C	50	A
Pulsed Collector Current	I <sub>C(PULSE)</sub>	Pulse width $\leq 1$ ms, duty cycle $\leq 1\%$	150	A
Diode Continuous Forward Current	$I_{\mathrm{F}}$	$T_C = 25$ °C	80 <sup>(1)</sup>	A
		$T_C = 100  ^{\circ}C$	50	A
Diode Pulsed Forward Current	I <sub>F(PULSE)</sub>	Pulse width $\leq 1$ ms, duty cycle $\leq 1\%$	150	A
Power Dissipation	$P_D$	$T_C = 25$ °C	240	W
Junction Temperature	$T_{\rm J}$		175	°C
Storage Temperature	$T_{STG}$		−55 to 150	°C

# **Thermal Resistance Characteristics**

Unless specifically noted,  $T_A = 25$  °C.

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Thermal Resistance of IGBT (Junction to Case)	$R_{\theta JC(IGBT)}$				0.62	°C/W
Thermal Resistance of Diode (Junction to Case)	$R_{ heta JC(DI)}$			_	0.87	°C/W

# **Mechanical Characteristics**

Parameter	Conditions	Min.	Тур.	Max.	Unit
Package Weight		_	6.1		g
Heatsink Mounting Screw Torque		0.686	_	0.882	N·m

<sup>(1)</sup> Limited by bonding wire.

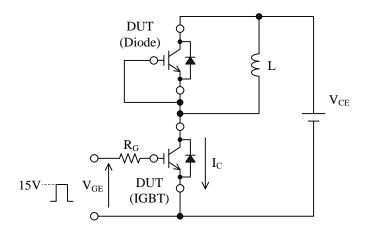
# KGF65B5H

# **Electrical Characteristics**

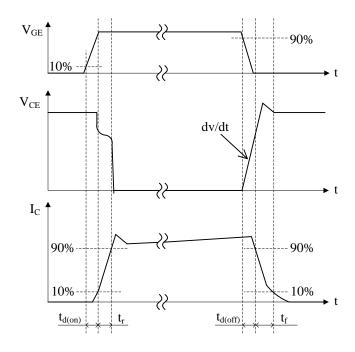
Unless otherwise specified,  $T_A = 25$  °C.

Parameter Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-to-Emitter Breakdown Voltage	V <sub>(BR)CES</sub>	$I_C = 100 \ \mu A, \ V_{GE} = 0 \ V$	650	_	_	V
Collector-to-Emitter Leakage Current	I <sub>CES</sub>	$V_{CE} = 650 \text{ V}, V_{GE} = 0 \text{ V}$	_	_	100	μΑ
Gate-to-Emitter Leakage Current	$I_{GES}$	$V_{GE} = \pm 20 \text{ V}$	_	_	±500	nA
Gate Threshold Voltage	$V_{\text{GE(TH)}}$	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$	4.0	5.5	7.0	V
Collector-to-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	$V_{GE} = 15 \text{ V}, I_{C} = 50 \text{ A}$	_	1.4	1.9	V
Input Capacitance	$C_{ies}$	$V_{CE} = 20 \text{ V},$	_	7500	_	pF
Output Capacitance	Coes	$V_{GE} = 0 V$ ,	_	400	_	pF
Reverse Transfer Capacitance	Cres	f = 100  kHz	_	300	_	pF
Total Gate Charge	Q <sub>G</sub>	$V_{CE} = 520 \text{ V},$ $I_{C} = 50 \text{ A}, V_{GE} = 15 \text{ V}$	_	310	_	nC
Turn-on Delay Time	$t_{d(\mathrm{ON})}$	T 25 °C	_	40	_	ns
Turn-on Rise Time	$t_{\rm r}$	$T_{C} = 25  ^{\circ}C,$ $V_{CE} = 400  V,$ $I_{C} = 50  A,$ $V_{GE} = 15  V,$ $R_{G} = 5  \Omega,$ $L = 100  \mu\text{H};$ see Figure 1	_	50	_	ns
Turn-off Delay Time	t <sub>d(OFF)</sub>		_	240	_	ns
Turn-off Fall Time	$t_{\mathrm{f}}$		_	50	_	ns
Turn-on Switching Losses (2)	Eon		_	1.9	_	mJ
Turn-off Switching Losses	E <sub>OFF</sub>		_	1.0	_	mJ
Turn-on Delay Time	t <sub>d(ON)</sub>	$T_{C} = 175 ^{\circ}\text{C},$ $V_{CE} = 400 \text{V},$ $I_{C} = 50 \text{A},$ $V_{GE} = 15 \text{V},$ $R_{G} = 5 \Omega,$ $L = 100 \mu\text{H},$	_	40	_	ns
Turn-on Rise Time	t <sub>r</sub>		_	50	_	ns
Turn-off Delay Time	t <sub>d(OFF)</sub>		_	250	_	ns
Turn-off Fall Time	$t_{\mathrm{f}}$		_	120		ns
Turn-on Switching Losses (2)	Eon		_	2.6	—	mJ
Turn-off Switching Losses	E <sub>OFF</sub>	see Figure 1	_	1.3	—	mJ
Diode Forward Voltage Drop	$V_{\mathrm{F}}$	$I_F = 50 A$	_	1.3	1.65	V
Diode Reverse Recovery Time	$t_{rr}$	$I_F = 50 \text{ A},$ $di/dt = 700 \text{ A/}\mu\text{s}$		120		ns

 $<sup>^{(2)}</sup>$  Energy losses include the reverse recovery of diode.



# (a) Test Circuit



(b) Waveform

Figure 1. Switching Time

# **Derating Curves**

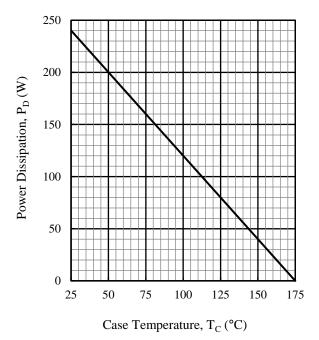
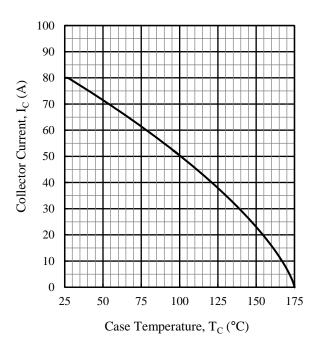


Figure 2.  $P_D$  vs.  $T_C$   $(T_J < 175 \, ^{\circ}C)$ 



 $\label{eq:figure 3.} \begin{array}{ll} Figure \ 3. & I_C \ vs. \ T_C \\ & (T_J < 175 \ ^{\circ}C) \end{array}$ 

# **Typical Characteristic Curves**

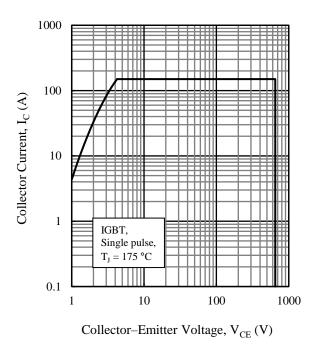


Figure 4. Reverse Bias Safe Operating Area

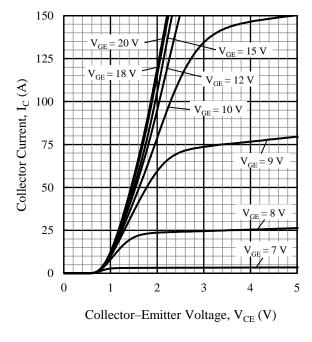


Figure 6. Typical Characteristics:  $I_C$  vs.  $V_{CE}$   $(T_J = 25 \, {}^{\circ}C)$ 

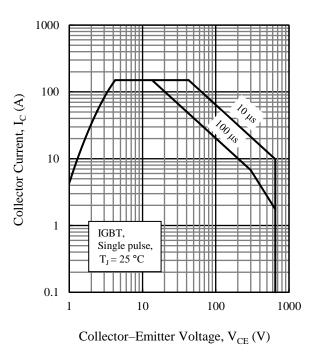


Figure 5. Safe Operating Area

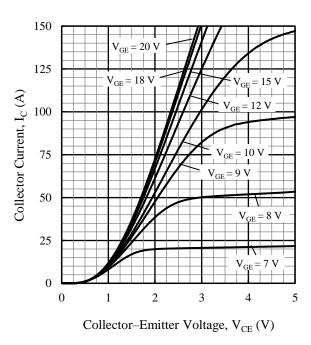


Figure 7. Typical Characteristics:  $I_C$  vs.  $V_{CE}$   $(T_J = 175 \, {}^{\circ}C)$ 

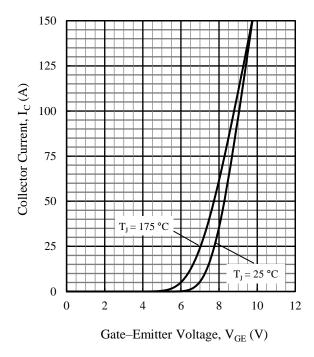


Figure 8. Typical Characteristics:  $I_C$  vs.  $V_{CE}$  ( $V_{CE} = 20$  V)

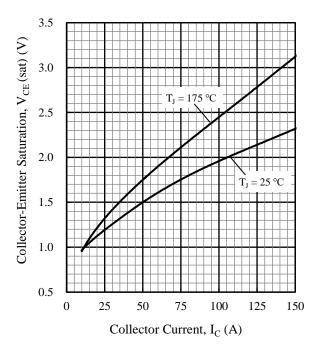


Figure 10. Typical Characteristics:  $V_{\text{CE(SAT)}}$  vs.  $I_{\text{C}}$  ( $V_{\text{GE}} = 15 \text{ V}$ )

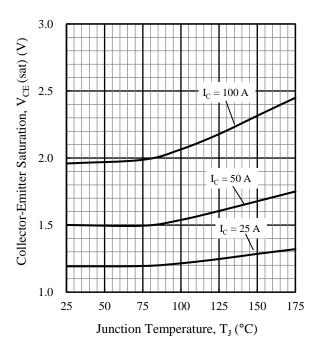


Figure 9. Typical Characteristics:  $V_{\text{CE(SAT)}}$  vs.  $T_J$  ( $V_{\text{GE}} = 15 \text{ V}$ )

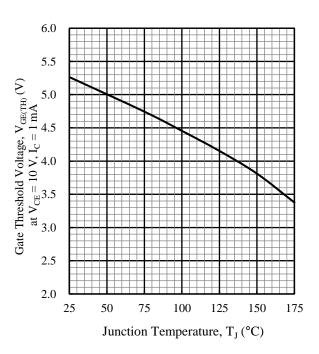


Figure 11. Typical Characteristics:  $V_{GE(TH)}$  vs.  $T_J$  ( $V_{CE} = 10 \text{ V}, I_C = 1 \text{ mA}$ )

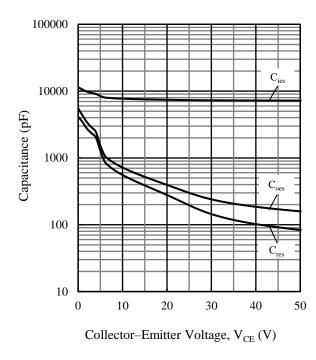


Figure 12. Typical Characteristics: Capacitance vs.  $V_{CE}$  (f = 100 kHz,  $V_{GE}$  = 0 V)

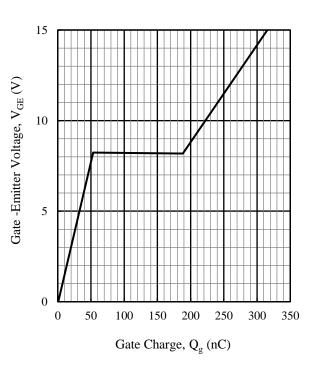


Figure 13. Typical Characteristics:  $V_{GE}$  vs.  $Q_G$  ( $I_C$  = 50 A,  $V_{CE}$   $\approx$  520 V)

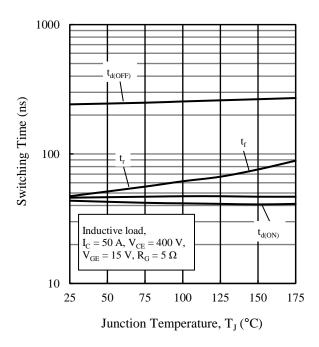


Figure. 14. Typical Characteristics: Switching Time vs. T<sub>J</sub>

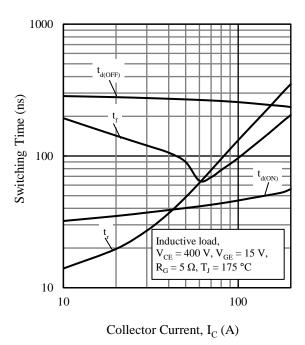


Figure 15. Typical Characteristics: Switching Time vs.  $I_C$ 

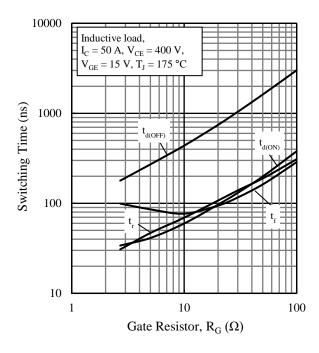


Figure 16. Typical Characteristics: Switching Time vs. R<sub>G</sub>

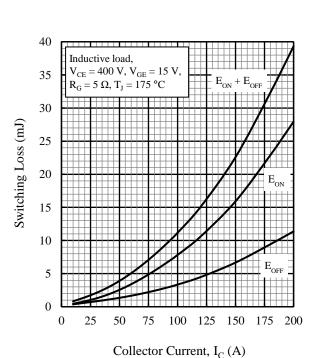


Figure 18. Typical Characteristics: Switching Loss vs.  $I_C$ 

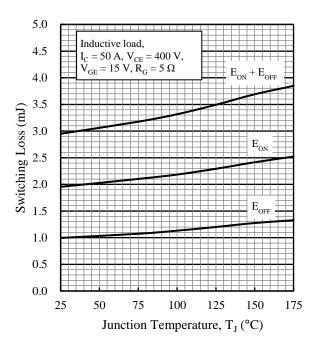


Figure 17. Typical Characteristics: Switching Loss vs. T<sub>J</sub>

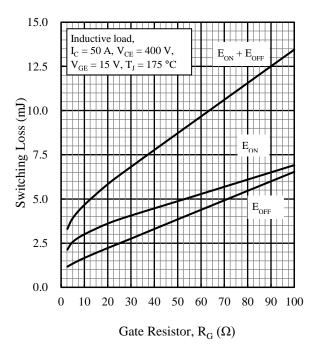


Figure 19. Typical Characteristics: Switching Loss vs. R<sub>G</sub>

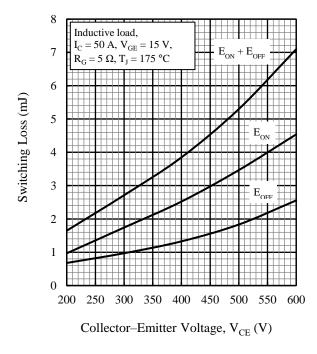
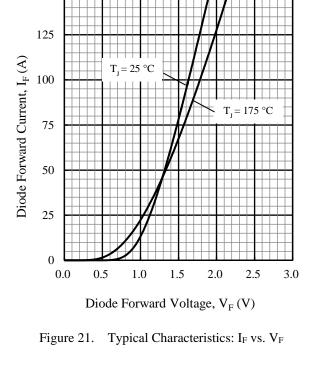


Figure 20. Typical Characteristics: Switching Loss vs.  $V_{\text{CE}}$ 



150

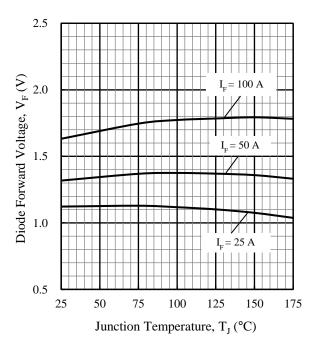


Figure 22. Typical Characteristics: V<sub>F</sub> vs. T<sub>J</sub>

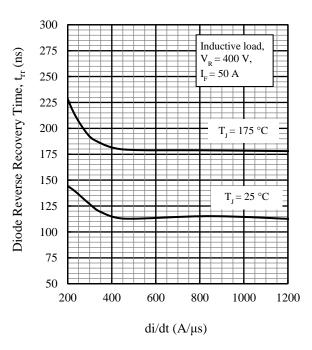
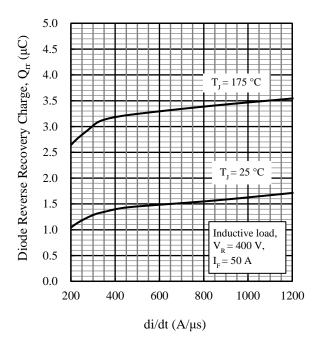


Figure 23. Typical Characteristics: t<sub>rr</sub> vs. di/dt



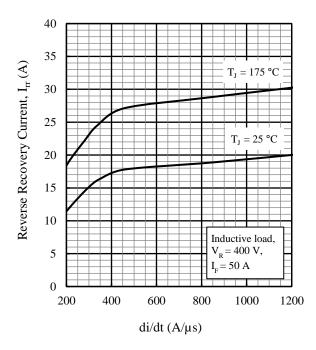


Figure 24. Typical Characteristics: Q<sub>rr</sub> vs. di/dt

Figure 25. Typical Characteristics: I<sub>rr</sub> vs. di/dt

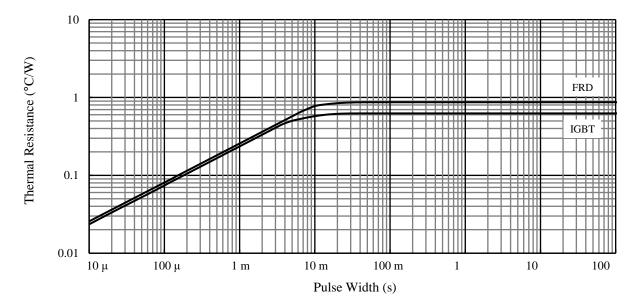
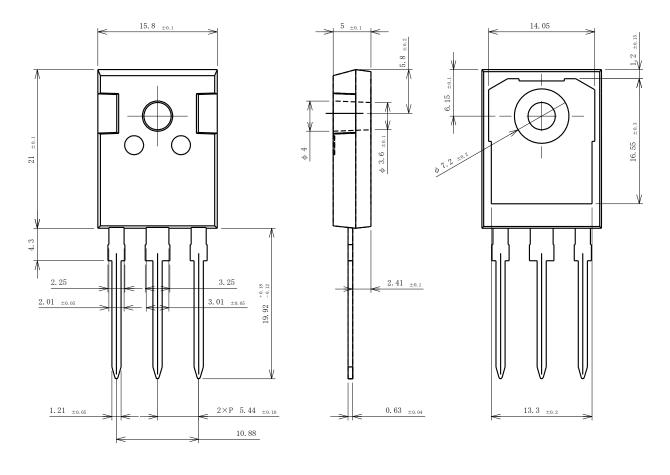


Figure 26. Transient Thermal Resistance Characteristics (Single Pulse,  $V_{CE} < 10 \text{ V}$ )

# **Physical Dimensions**

### • TO247-3L





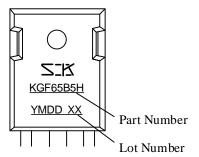
#### **NOTES:**

- Dimensions in millimeters
- Excludes mold flash
- Dimensions in millimeters
- Bare lead: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time within the following limits:

Flow:  $270 \, ^{\circ}\text{C} / 7 \, \text{s}$ , 1 time

Soldering Iron: 350  $^{\circ}$ C / 3.5 s, 1 time (Soldering should be at a distance of at least 1.5 mm from the body of the product.)

# **Marking Diagram**



Y is the last digit of the year of manufacture (0 to 9)

M is the month of the year (1 to 9, O, N, or D)

DD is the day of the month (01 to 31)

XX is the control number

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