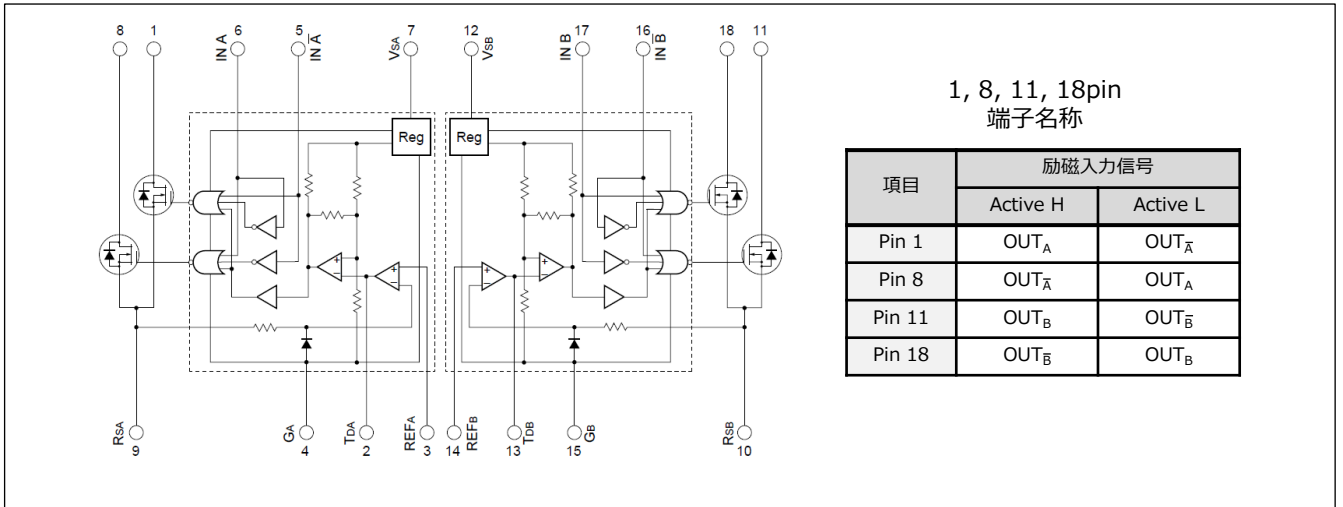


(Ta = 25 °C)

		SLA7027MU			SLA7024M			SLA7026M			
		min.	typ.	max.	min.	typ.	max.	min.	typ.	max.	
	V _{CC}	46									V
FET	V _{DSS}	100									V
	V _S	46									V
	V _{IN}	7									V
	V _{REF}	2									V
	I _O	1.0			1.5			3.0			A
	P _{D1}	4.5 (Without Heatsink)									W
	P _{D2}	35 (Tc = 25 °C)									W
	T _{ch}	+150									
	T _{stg}	-40 +150									

		SLA7027MU									SLA7024M			SLA7026M						
		min.			typ.			max.			min.			typ.				max.		
		min.	typ.	max.	min.	typ.	max.	min.	typ.	max.	min.	typ.	max.	min.	typ.	max.		min.	typ.	max.
DC	I _S	10			15			10			15			10			15			mA
		V _S =44V									V _S =44V			V _S =44V						
FET	V _S	10	24	44	10	24	44	10	24	44	10	24	44	10	24	44	10	24	44	V
	V _{DSS}	100			100			100			100			100			100			V
FET ON	V _{DS}	0.85			0.6			0.85			0.6			0.85			0.6			V
		I _D =1A, V _S =14V									I _D =1A, V _S =14V			I _D =3A, V _S =14V						
FET	I _{DSS}	4			4			4			4			4			4			mA
		V _{DSS} =100V, V _S =44V									V _{DSS} =100V, V _S =44V			V _{DSS} =100V, V _S =44V						
FET	V _{SD}	1.2			1.1			1.2			1.1			1.2			1.1			V
		I _D =1A									I _D =1A			I _D =3A						
TTL	I _{IH}	40			40			40			40			40			40			5
		V _{IH} =2.4V, V _S =44V									V _{IH} =2.4V, V _S =44V			V _{IH} =2.4V, V _S =44V						
TTL	I _{IL}	-0.8			-0.8			-0.8			-0.8			-0.8			-0.8			mA
		V _{IL} =0.4V, V _S =44V									V _{IL} =0.4V, V _S =44V			V _{IL} =0.4V, V _S =44V						
TTL (Active High)	V _{IH}	2			2			2			2			2			2			V
		I _D =1A									I _D =1A			I _D =3A						
		0.8			0.8			0.8			0.8			0.8						
TTL (Active Low)	V _{IL}	2			2			2			2			2			2			V
		V _{DSS} =100V									V _{DSS} =100V			V _{DSS} =100V						
		0.8			0.8			0.8			0.8			0.8						
AC	T _r	0.5			0.5			0.5			0.5			0.5			0.5			g
		V _S =24V, I _D =0.8A									V _S =24V, I _D =1A			V _S =24V, I _D =1A						
	T _{stg}	0.7			0.7			0.7			0.7			0.7			0.7			
		V _S =24V, I _D =0.8A									V _S =24V, I _D =1A			V _S =24V, I _D =1A						
T _f	0.1			0.1			0.1			0.1			0.1			0.1				
	V _S =24V, I _D =0.8A									V _S =24V, I _D =1A			V _S =24V, I _D =1A							

■ 内部ブロック図



■ 標準外付け回路図(推奨定数)

Active High

励磁信号タイムチャート

2相励磁

Clock	0	1	2	3	0	1
IN _A	H	L	L	H	H	L
IN _A [¯]	L	H	H	L	L	H
IN _B	H	H	L	L	H	H
IN _B [¯]	L	L	H	H	L	L

1-2相励磁

Clock	0	1	2	3	4	5	6	7	0	1	2	3
IN _A	H	H	L	L	L	L	L	H	H	H	L	L
IN _A [¯]	L	L	L	H	H	H	L	L	L	L	L	H
IN _B	L	H	H	H	L	L	L	L	L	H	H	H
IN _B [¯]	L	L	L	L	L	H	H	H	L	L	L	L

Active Low

励磁信号タイムチャート

2相励磁

Clock	0	1	2	3	0	1
IN _A	L	H	H	L	L	H
IN _A [¯]	H	L	L	H	H	L
IN _B	L	L	H	H	L	L
IN _B [¯]	H	H	L	L	H	H

1-2相励磁

Clock	0	1	2	3	4	5	6	7	0	1	2	3
IN _A	L	L	H	H	H	H	H	L	L	L	H	H
IN _A [¯]	H	H	H	L	L	L	H	H	H	H	H	L
IN _B	H	L	L	L	H	H	H	H	H	L	L	L
IN _B [¯]	H	H	H	H	H	L	L	L	H	H	H	H

r1 : 510Ω
r2 : 100Ω (VR)
r3 : 47kΩ
r4 : 47kΩ
r5 : 2.4kΩ
r6 : 2.4kΩ
C1 : 470pF
C2 : 470pF
C3 : 2200pF
C4 : 2200pF
Rs : 1Ω typ(7024M)
(1~2W) 0.68Ω typ(7026M)
1.8Ω typ(7027MU)

■ 外形図

